

# **[ DETECTION CIRCUIT AND METHOD FOR CLEARING BIOS CONFIGURATION MEMORY ]**

## **Abstract of Disclosure**

A BIOS configuration memory-clearing detection circuit and a method of clearing a BIOS configuration memory for computer main board uses a latching circuit to detect whether a user has set a clearing of the BIOS configuration memory. If the user has set a clearing of the BIOS configuration memory, the latching circuit sets a clearing latch signal. When the computer subsequently switches on, a ROM BIOS reads the status of the clearing latch signal. If the clearing latch signal is set, the ROM BIOS further clears the BIOS configuration memory to ensure the clearing operation is successful.

## Figures